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PRINthead HAVING EMBEDDED MEMORY DEVICE

ABSTRACT OF THE DISCLOSURE

A semiconductor substrate for a micro-fluid ejecting device. The semiconductor substrate includes a plurality of fluid ejection devices disposed on the substrate. A plurality of driver transistors are disposed on the substrate for driving the plurality of fluid ejection devices. A programmable memory matrix containing embedded 5 programmable memory devices is operatively connected to the micro-fluid ejecting device for collecting and storing information on the semiconductor substrate for operation of the micro-fluid ejecting device. The programmable memory matrix provides a high density of memory bits embedded on the substrate for storing information about the micro-fluid ejecting device.

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